

FIG. 1 (PRIOR ART)

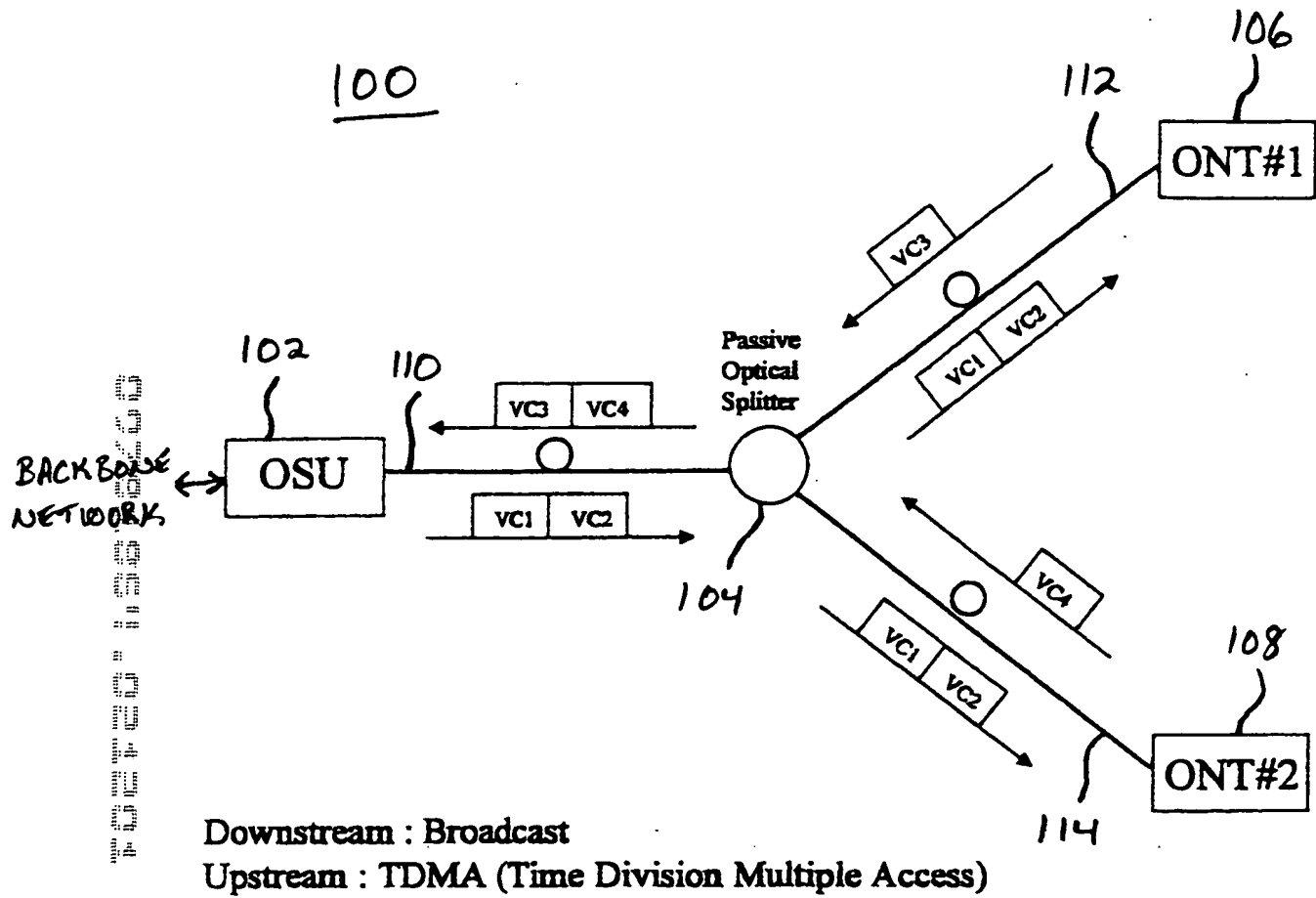


FIG. 2 (PRIOR ART)

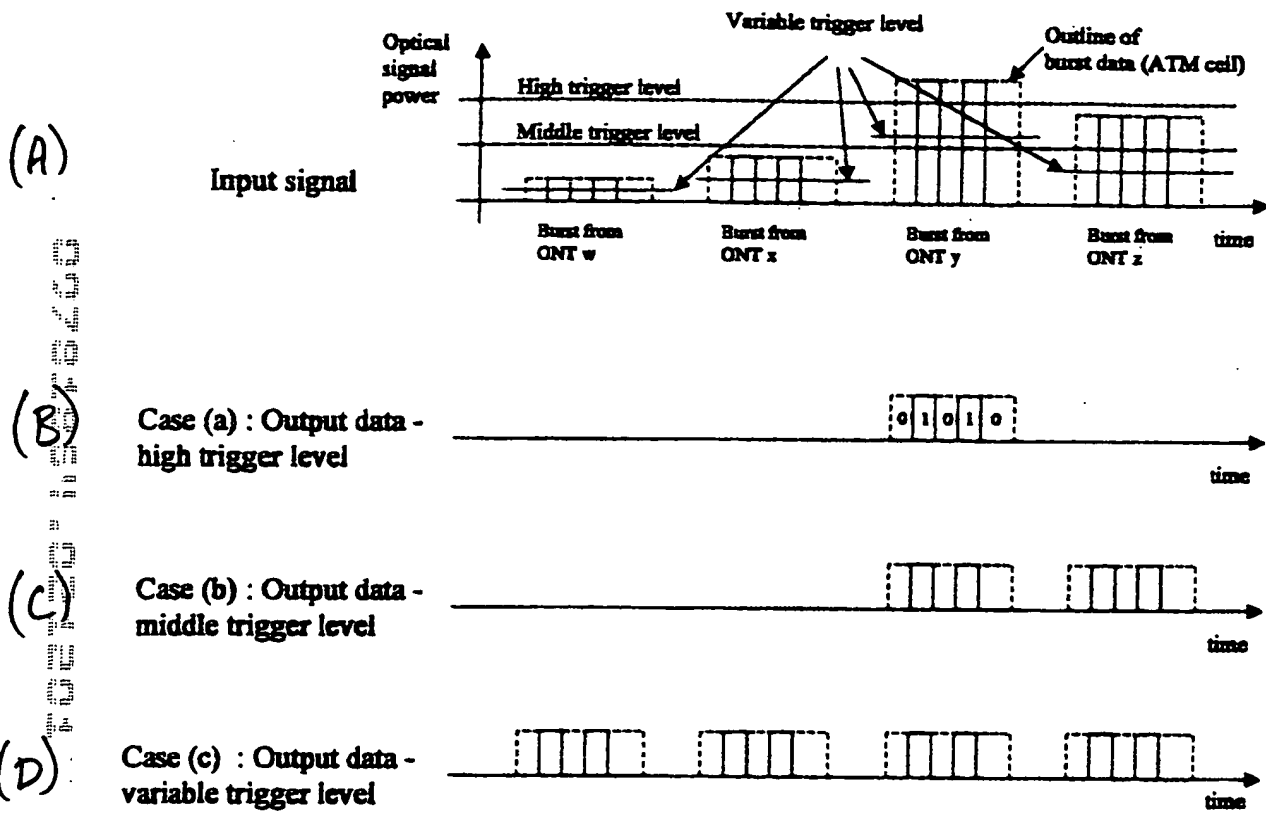


FIG. 3

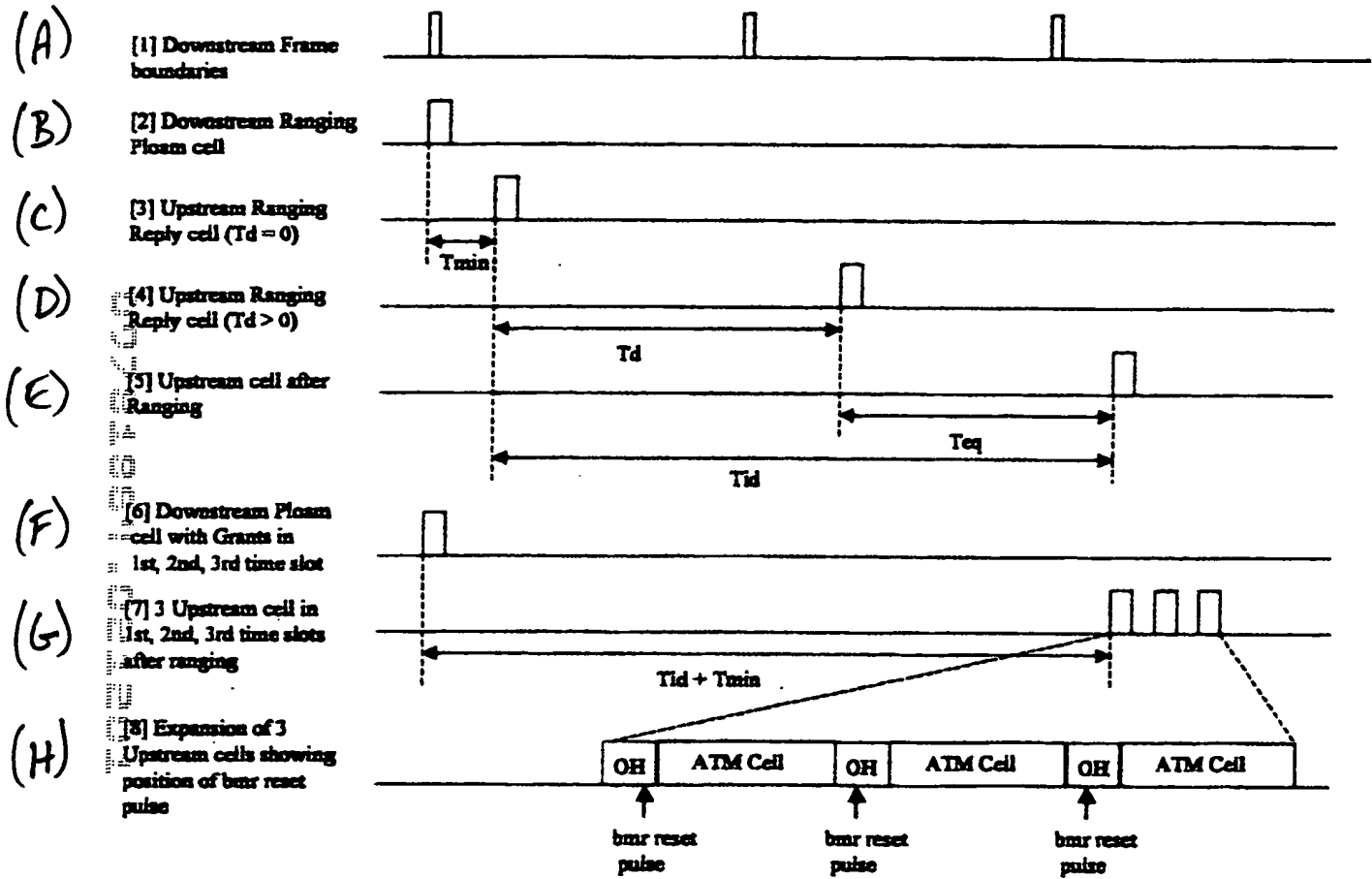


FIG. 4

400

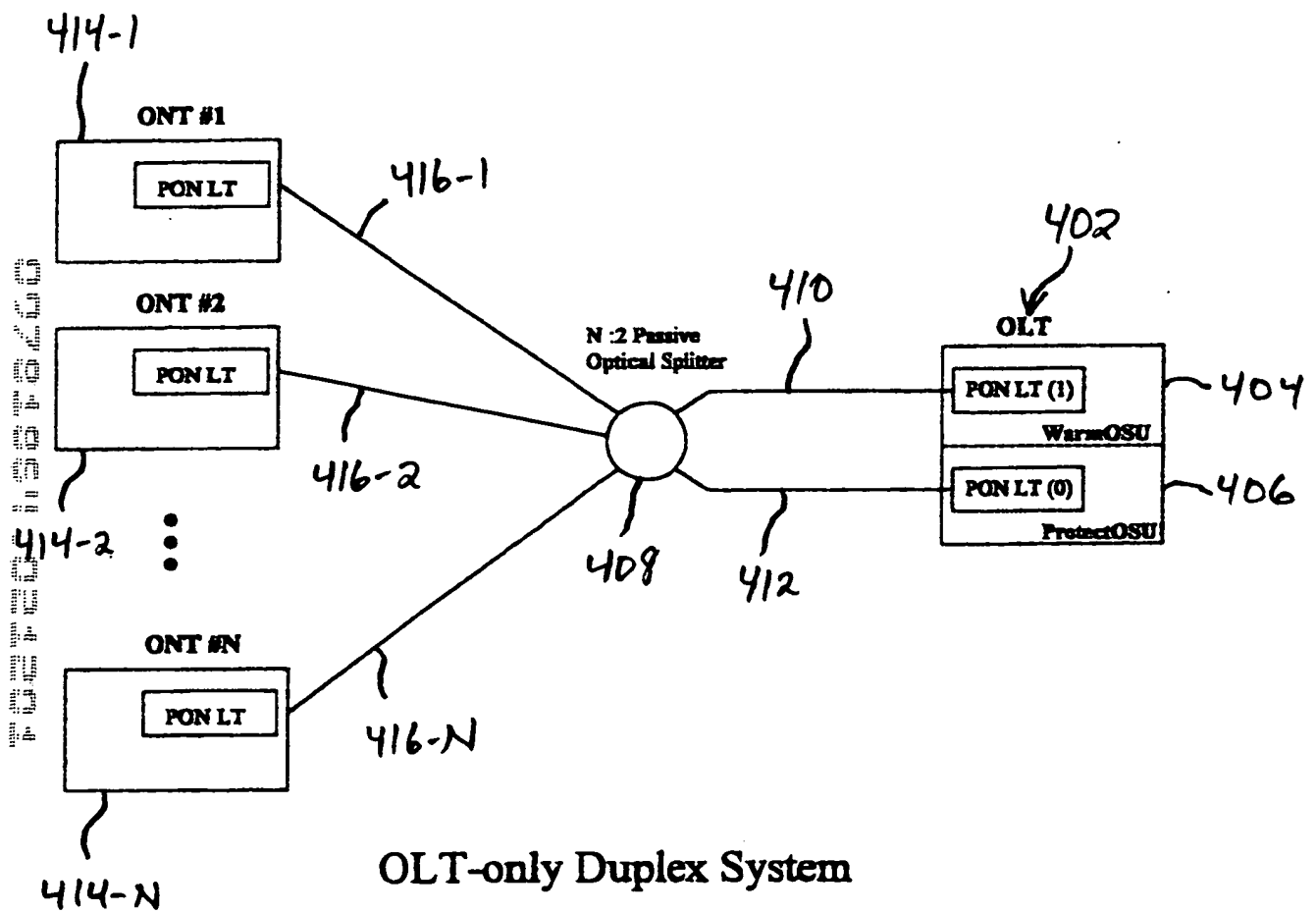
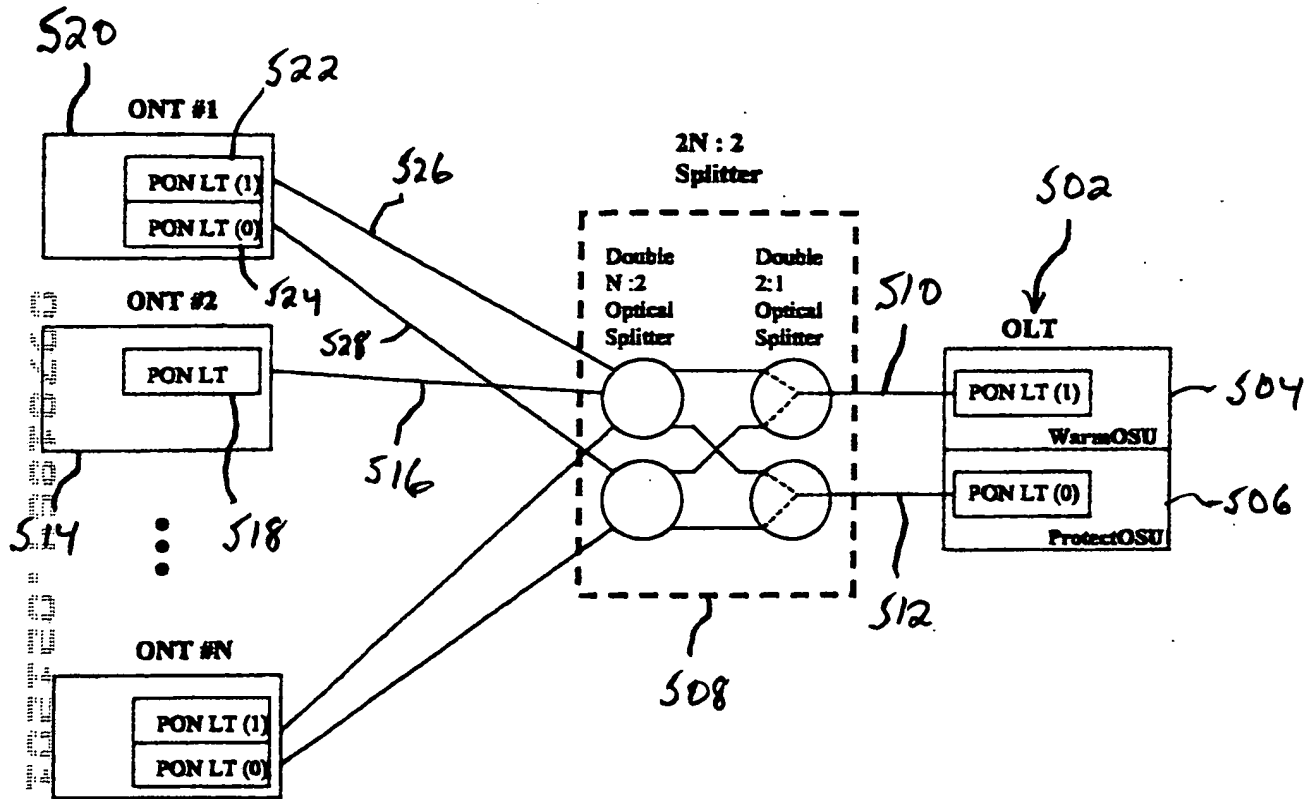


FIG. 5

500



Partial Duplex System

FIG. 6

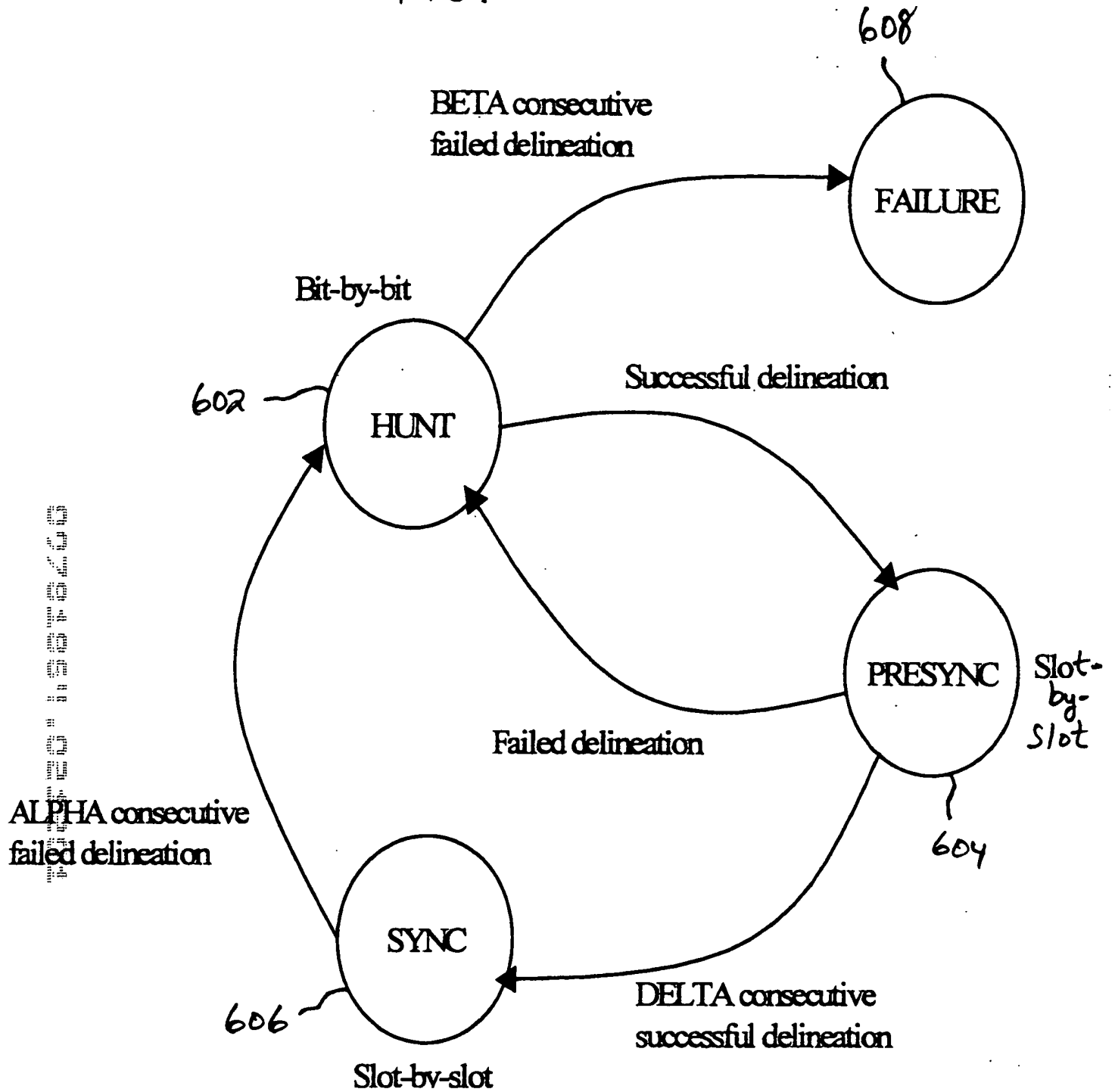




FIG. 8

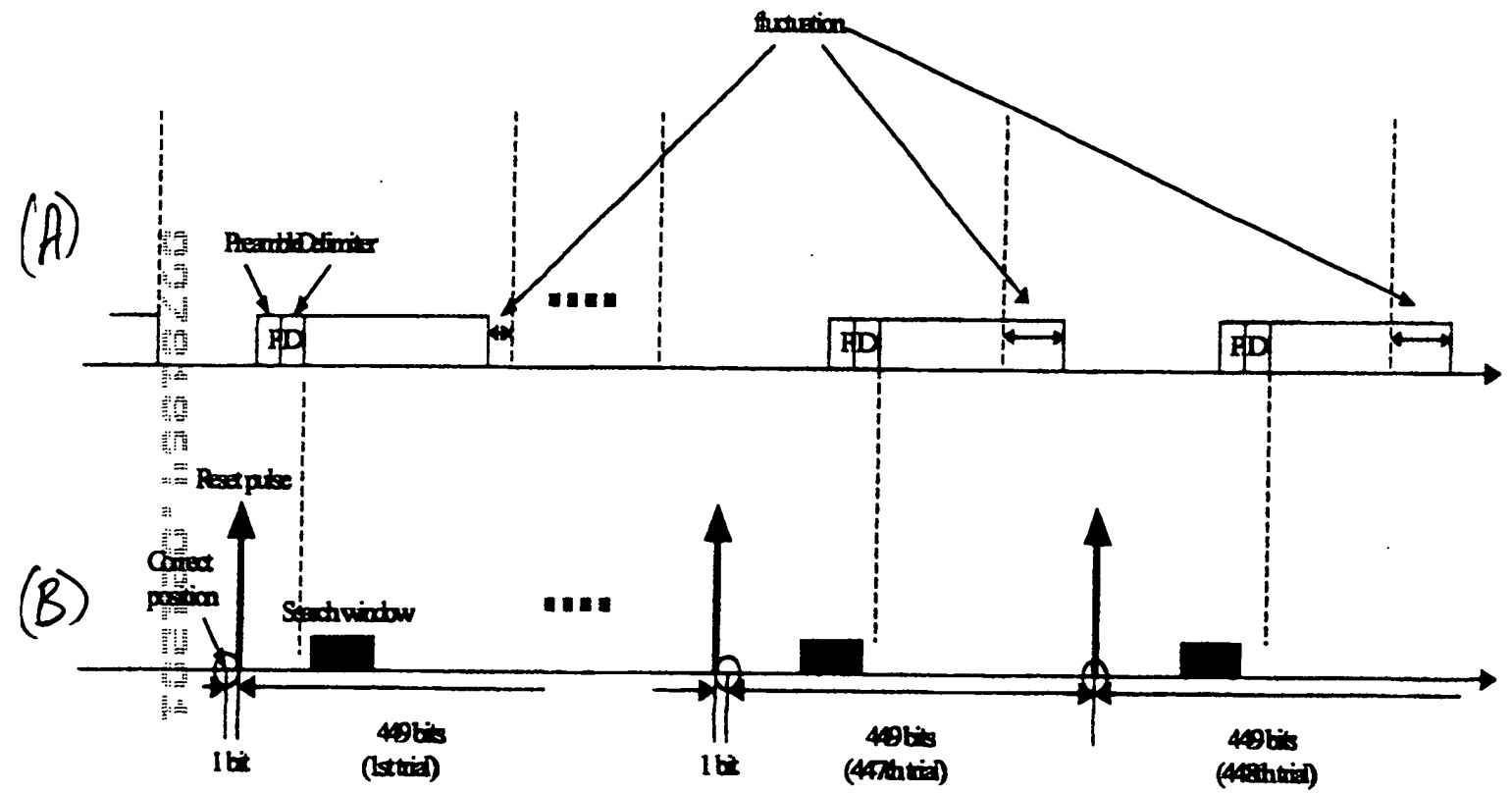


FIG. 9

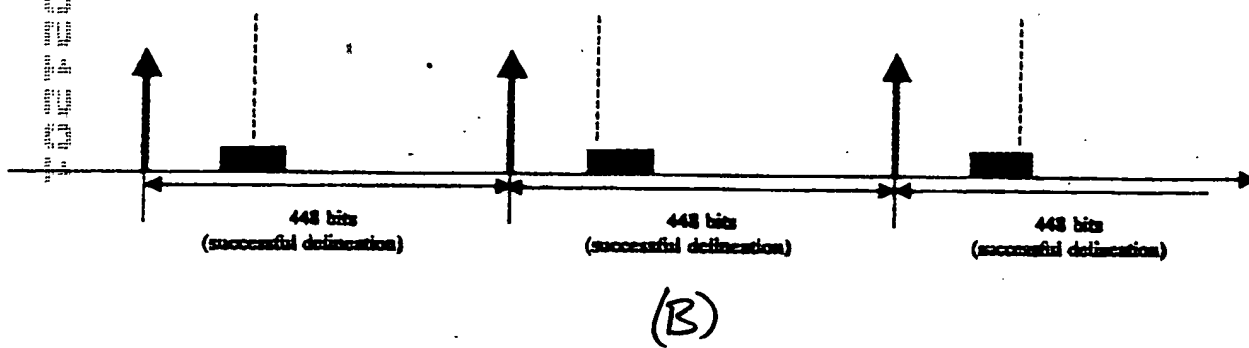
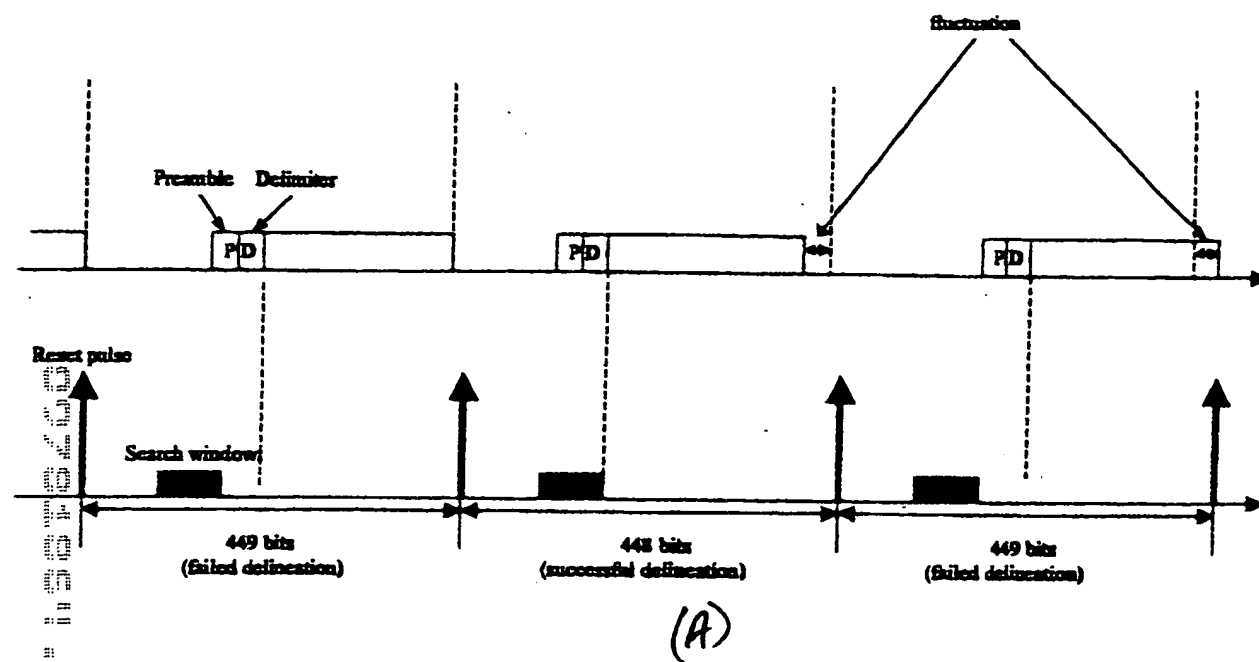


FIG. 10

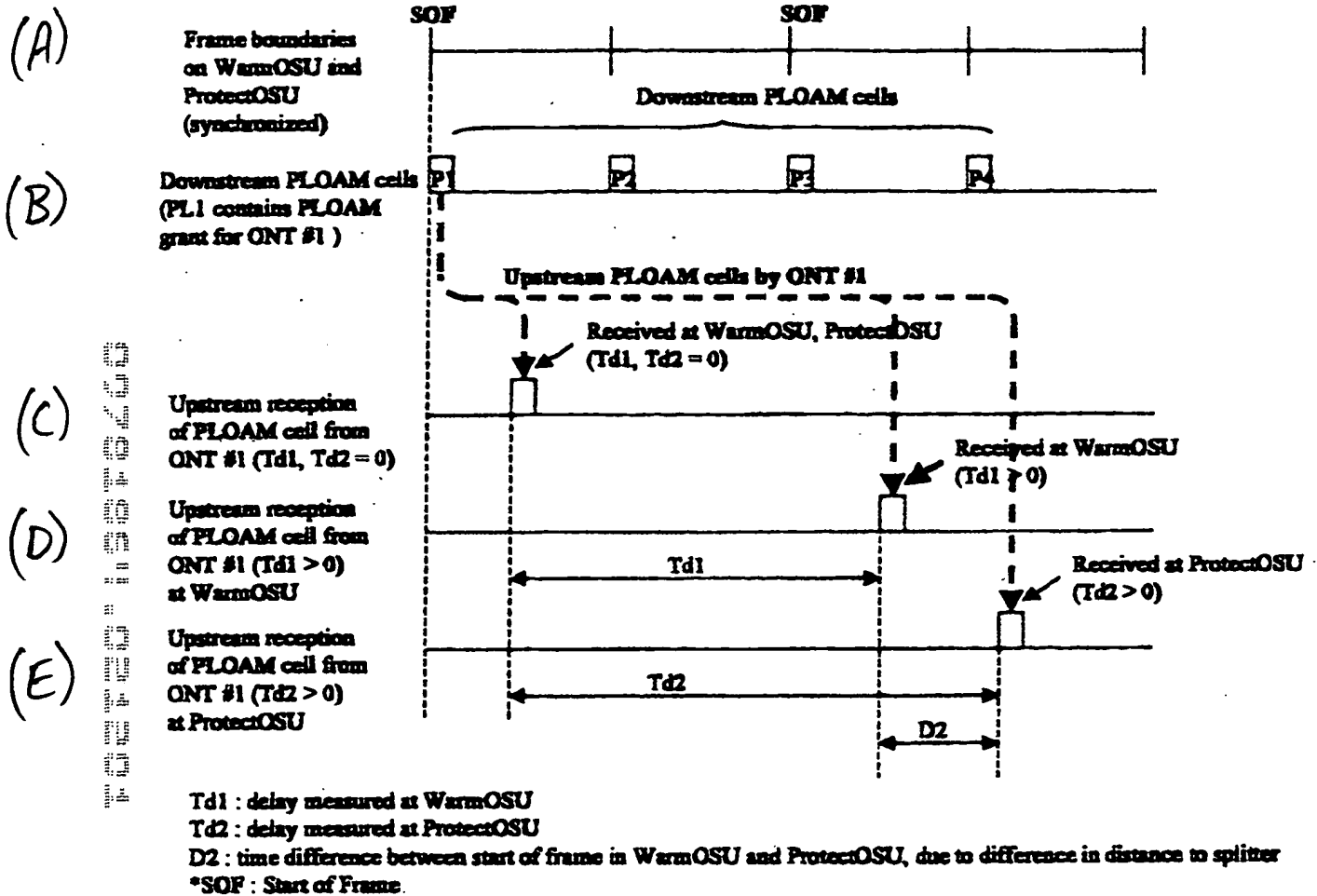
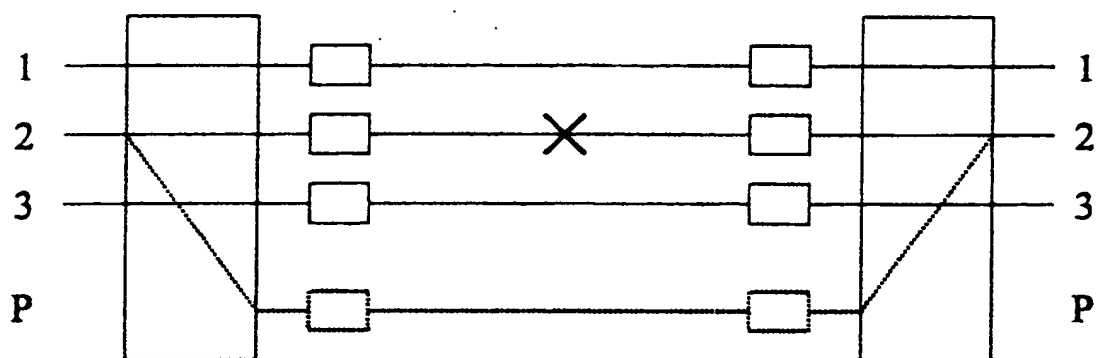


FIG. 11



1:N Protection (example N = 3)

FIG. 12

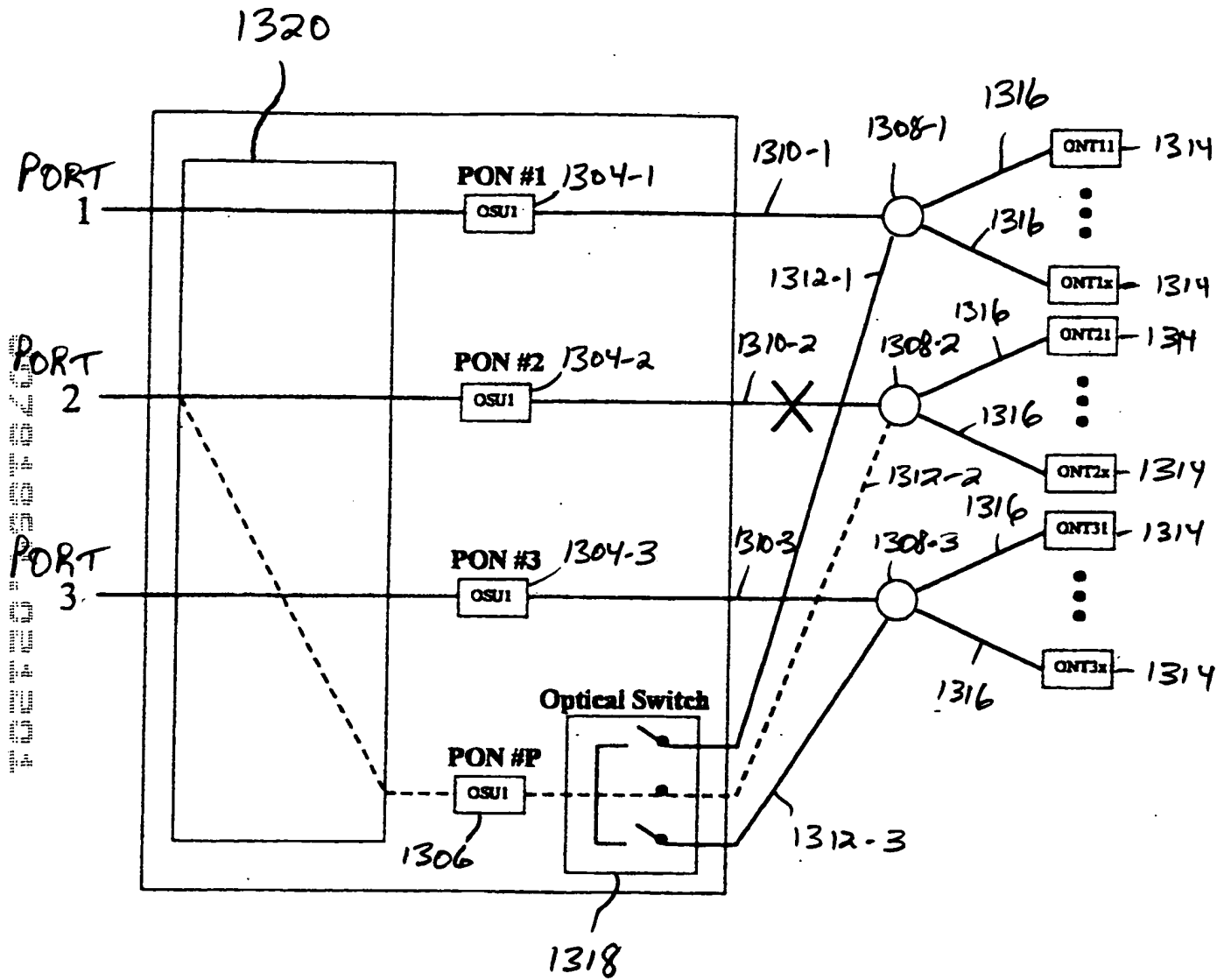
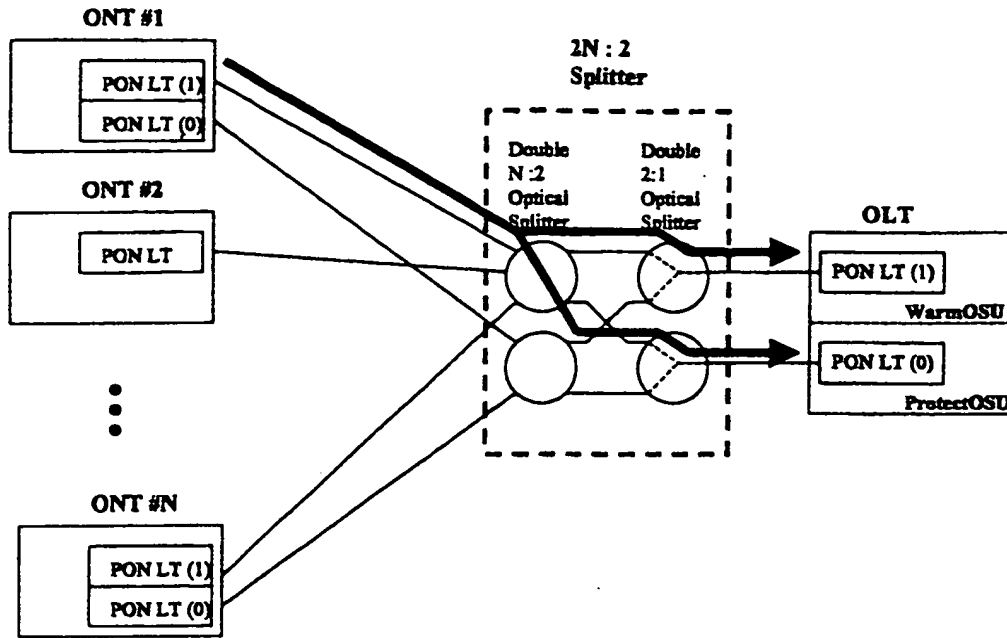
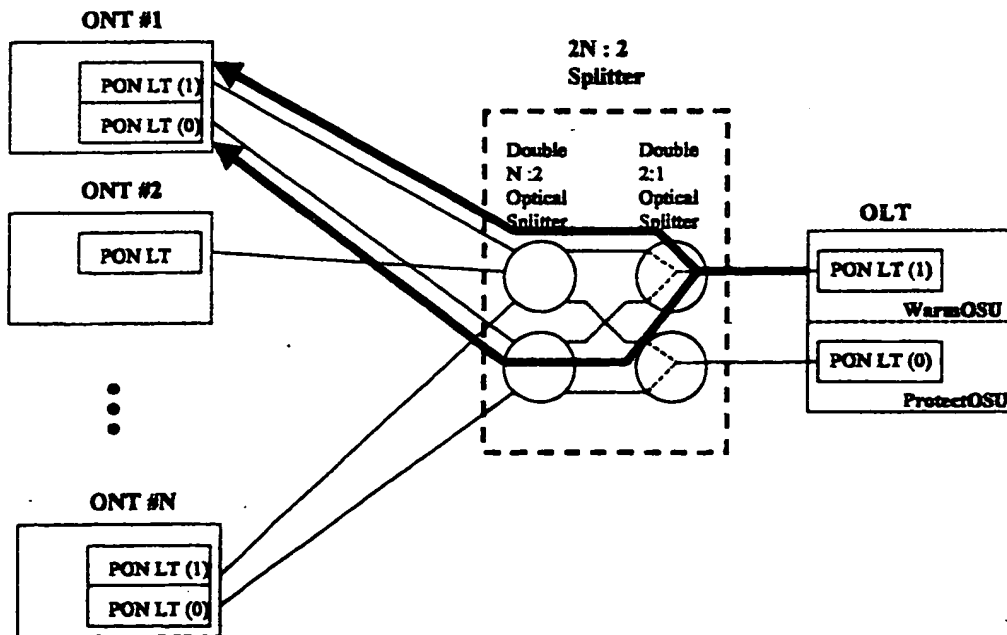


FIG. 13



Partial Duplex System

FIG. 14



Partial Duplex System